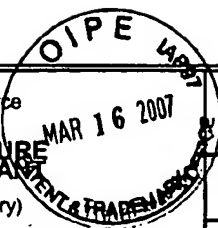


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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/AF/	6 9 5 2 4 4 1	10/2005	Peeters			

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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO
2001 2 9 8 4 3 9	10/2001	JP			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Japanese Office Action dated November 22, 2006 with English translation.

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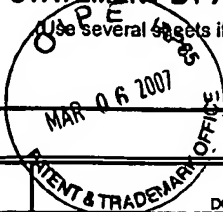
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO
/AF/	1 1 2 7 5 1 6 5	10/1999	JP			XX
/AF/	1 1 2 5 2 0 3 1	09/1999	JP			XX

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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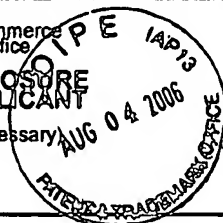
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/AF/	6 4 7 3 4 0 9	10/2002	Malvar			
/AF/	6 4 8 7 5 7 4	11/2002	Malvar			
/AF/	6 4 9 6 7 9 5	12/2002	Malvar			

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/AF/	J. Alhava, et al., "Adaptive Sine-Modulated/Cosine-Modulated Filter Bank Equalizer for Transmultiplexers," European Conference on Circuit Theory and Design, Espoo, Finland, Aug. 28-31, 2001, pp. 337-340.
/AF/	A. Viholainen, et al., "Implementation of Parallel Cosine and Sine Modulated Filter Banks for Equalized Transmultiplexer Systems," Telecommunications Laboratory, Tampere University of Technology, Tampere, Finland, IEEE, 2001, pp. 3625-2628.
/AF/	A. Viholainen, et al., "Complex Modulated Critically Sampled Filter Banks Based on Cosine and Sine Modulation," Institute of Communications Engineering, Tampere University of Technology, Tampere, Finland, IEEE, 2002, pp. 833-836.
/AF/	J. Alhava, et al., "Exponentially-Modulated Filter Bank-Based Transmultiplexer," Tampere University of Technology, Institute of Communications Engineering, Tampere, Finland, IEEE, 2003, pp. 233-236.
/AF/	J. Alhava, et al., "Efficient Implementation of Complex Exponentially-Modulated Filter Banks," Tampere University of Technology, Institute of Communications Engineering, Tampere, Finland, 2003, pp. 157-160.
/AF/	Y. Yang, et al., "DSP Implementation of Low-Complexity Equalizer for Multicarrier Systems," Institute of Communications Engineering, Tampere University of Technology, IEEE, 2003, pp. 271-274.

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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION									
					YES	NO								

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Power-Line Communication (including Power, Price, Data, Performance, Range, Size)
/AF/		M. C. Sun, et al., "Power-Line Communication using DWT Modulation," 2002 IEEE International Symposium on Circuits and Systems, vol. 4, XP002271367, pages 493-496, May 26-29, 2002.

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	2004 0 0 5 7 5 2 9	03/2004	Koga et al.			
	2002 0 0 0 3 8 4 3	01/2002	Martone			

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/AF/	0 3 0 6 3 3 8 0	07/2003	WO			
/AF/	2 8 0 0 9 5 4	05/2001	FR			
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/AF/	International Search Report dated April 26, 2004
/AF/	Ozturk E. et al.: "Waveform Encoding of Binary Signals Using a Wavelet and its Hilbert Transform"; vol. 5, 5 June 2000, pages 2641-2644

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/AF/	5 6 3 1 6 1 0	05/20/97	Sandberg et al.			
/AF/	5 6 3 6 2 4 6	06/03/97	Tzannes et al.			
/AF/	5 4 9 7 3 9 8	03/05/96	Tzannes et al.			
/AF/	6 5 3 2 2 5 6	03/11/03	RAMer			
/AF/	5 9 9 5 5 3 9	11/30/99	RAMer			

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/AF/	Malvar, Henrique S., "Signal Processing with Lapped Transforms," ISBN 089006-467-9, ppgs. 204-219, 1992.

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